IN THE CLAIMS

1. (Previously Presented) A termination stub system comprising:

a first resistor for dampening reflections of a signal, said first resistor in series with an input signal path;

a division point for dividing said signal into a plurality of output communication paths, wherein said plurality of output communication paths are substantially the same length, said division point coupled to said first resistor; and

a second resistor for balancing resistance of a termination stub system with a characteristic impedance of said signal input path, said second resistor in parallel with said input signal path and said plurality of output communication paths and said second resistor coupled to said first resistor.

2. (Original) A termination stub system of Claim 1 wherein said first resistor and said second resistor form a voltage divider for reducing a voltage level of said signal at said division point.

3. (Original) A termination stub system of Claim1 wherein said first resistor is sized to reduce overshoot of said signal at receivers coupled to said plurality of output communication paths.

4. (Original) A termination stub system of Claim 1 wherein said second resistor is coupled to a termination voltage.

5. (Original) A termination stub system of Claim 5 wherein said termination voltage is a steady state voltage.

6. (Previously Presented) A termination stub system of Claim 1 wherein said input signal path and said plurality of output communication paths are trace lines in a printed circuit board.

7. (Original) A termination stub system of Claim 1 wherein said first resistor is coupled immediately to said second resistor.

8. (Previously Presented) An integrated circuit comprising:

a plurality of receivers for receiving a signal;

a driver for driving said signal to said plurality of receivers; and

a termination stub system for directing said signal to said receivers while minimizing reflection of said signal towards said driver, wherein said termination stub system includes a plurality of substantially equal length branch signal communication paths coupled to said plurality of receivers.

9. (Previously Presented) An integrated circuit of claim 8 wherein said integrated circuit is a printed circuit board.

10. (Original) An integrated circuit of claim 8 wherein said plurality of branch signal communication paths are the same length.

11. (Original) An integrated circuit of claim 10 wherein said plurality of branch signal communication paths are configured to deliver said signal.

12. (Original) An integrated circuit of claim 11 wherein said signal is a control signal.

13. (Original) An integrated circuit of claim 12 wherein said receivers are memory components included in a memory system and said control signal controls said memory components.

14. (Original) An integrated circuit of claim 13 wherein said control signals are chip select signals.

15. (Previously Presented) A termination stub method comprising:

forwarding a signal to a single distribution point;

distributing said signal to a plurality of destinations;

reducing reflectance of said signal by creating a termination voltage and matching a characteristic transmission impedance; and

directing said signal along a plurality of communication paths that are substantially the same length.

16. (Original) A termination stub method of claim 15 further comprising managing overshoot conditions.

17. (Previously Presented) A termination stub method of claim 15 wherein said distributing further comprises:

receiving a signal along a single path; and

splitting said signal into a plurality of signal wave fronts along said plurality of communication paths.

18. (Cancelled)

19. (Previously Presented) A termination stub method of claim 15 wherein wave fronts of said signal are received concurrently.

20. (Cancelled).